

~~PROPRIETARY~~ Rec'd 11 MAY 2000

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yoichi HIJIKATA

Application No.: 09/424,670

Filed: November 29, 1999

Docket No.: 104824

For: MICROCOMPUTER, ELECTRONIC EQUIPMENT, AND DEBUGGING SYSTEM

PRELIMINARY AMENDMENT

Director of the U.S. Patent and Trademark Office
Washington, D. C. 20231

Sir:

Prior to initial examination, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please amend the specification as follows:

Page 1, line 17, change "Fig. 1A" to --Fig. 1--.

Page 5, lines 7-9, change "; and the debugging terminal connected to a single communications line for transferring the data in a half-duplex bidirectional manner," to --, a single communications line for transferring the data in a half-duplex bidirectional manner is connected to the debugging terminal,--.

Page 16, line 1, change "the CPU 12" to --the CPU 22--.

Page 20, line 25, "A clock generation section 118" to --A clock generation section 119--.

Page 22, line 23, change "the monitor RAM 44" to --the mini monitor RAM 44--.